N-Channel 40-V (D-S) MOSFET

RoHS COMPLIANT

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (mΩ)(Typ.)	I_D (A)[a, e]	Q_g (Typ.)
40	7.3 at V_{GS} = 10 V	65	29 nC
	10.8 at V_{GS} = 4.5 V	55	

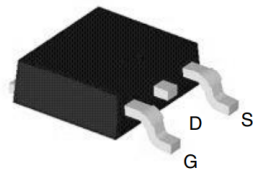
FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2011/65/EU

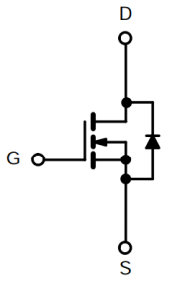
APPLICATIONS

- OR-ing
- Server
- DC/DC

TO-252 Pin Configuration



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)				
Parameter		**Symbol**	**Limit**	**Unit**
Drain-Source Voltage		V_{DS}	40	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current (T_J = 175 °C)	T_C = 25 °C	I_D	65[a, e]	A
	T_C = 70 °C		50[e]	
	T_A = 25 °C		20.8[b, c]	
	T_A = 70 °C		19[b, c]	
Pulsed Drain Current		I_{DM}	195	
Avalanche Current Pulse	L = 0.1 mH	I_{AS}	60	
Single Pulse Avalanche Energy		E_{AS}	142	mJ
Continuous Source-Drain Diode Current	T_C = 25 °C	I_S	65[a, e]	A
	T_A = 25 °C		2.3[b, c]	
Maximum Power Dissipation	T_C = 25 °C	P_D	80[a]	W
	T_C = 70 °C		51	
	T_A = 25 °C		3.05[b, c]	
	T_A = 70 °C		1.95[b, c]	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter		**Symbol**	**Typ.**	**Max.**	**Unit**
Maximum Junction-to-Ambient[b, d]	t ≤ 10 sec	R_{thJA}	32	40	°C/W
Maximum Junction-to-Case	Steady State	R_{thJC}	0.5	0.6	

Notes:
a. Based on T_C = 25 °C.
b. Surface mounted on 1" x 1" FR4 board.
c. t = 10 sec.
d. Maximum under steady state conditions is 90 °C/W.
e. Calculated based on maximum junction temperature.

SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)						
Parameter	**Symbol**	**Test Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA	40			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	I_D = 250 µA		35		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 7.5		
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	1.0		3.0	V
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 40 V, V_{GS} = 0 V			1	µA
		V_{DS} = 32 V, V_{GS} = 0 V, T_J = 55 °C			10	
On-State Drain Current[a]	$I_{D(on)}$	$V_{DS} \geq$ 5 V, V_{GS} = 10 V	65			A
Drain-Source On-State Resistance[a]	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 30 A		7.3	8.5	mΩ
		V_{GS} = 4.5 V, I_D = 20 A		10.8	12	
Forward Transconductance[a]	g_{fs}	V_{DS} = 5 V, I_D = 30 A		50		S
Dynamic[b]						
Input Capacitance	C_{iss}	V_{DS} = 20 V, V_{GS} = 0 V, f = 1 MHz		1790		pF
Output Capacitance	C_{oss}			134		
Reverse Transfer Capacitance	C_{rss}			112		
Total Gate Charge	Q_g	V_{DS} = 20 V, V_{GS} = 10 V, I_D = 30 A		29		nC
Gate-Source Charge	Q_{gs}			3		
Gate-Drain Charge	Q_{gd}			11.5		
Gate Resistance	R_g	f = 1 MHz		2.8		Ω
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 20 V, R_L = 0.625 Ω $I_D \cong$ 30 A, V_{GEN} = 10 V, R_g = 1 Ω		18		ns
Rise Time	t_r			11		
Turn-Off Delay Time	$t_{d(off)}$			70		
Fall Time	t_f			10		
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 20 V, R_L = 0.67 Ω $I_D \cong$ 20 A, V_{GEN} = 4.5 V, R_g = 1 Ω		55		
Rise Time	t_r			180		
Turn-Off Delay Time	$t_{d(off)}$			55		
Fall Time	t_f			12		
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	T_C = 25 °C			65	A
Pulse Diode Forward Current[a]	I_{SM}				195	
Body Diode Voltage	V_{SD}	I_S = 30 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = 30 A, di/dt = 100 A/µs, T_J = 25 °C		40		ns
Body Diode Reverse Recovery Charge	Q_{rr}			66		nC
Reverse Recovery Fall Time	t_a			20		ns
Reverse Recovery Rise Time	t_b			19		

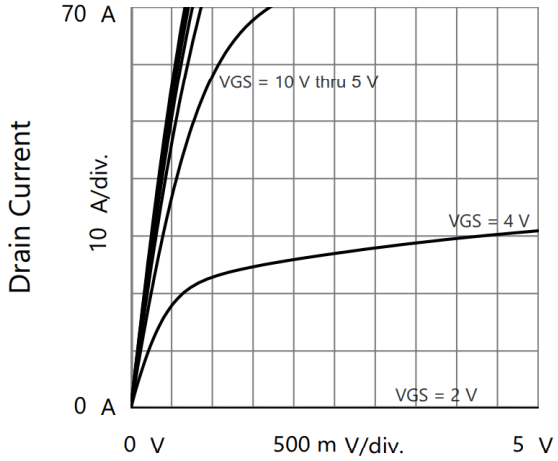
Notes:

a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.

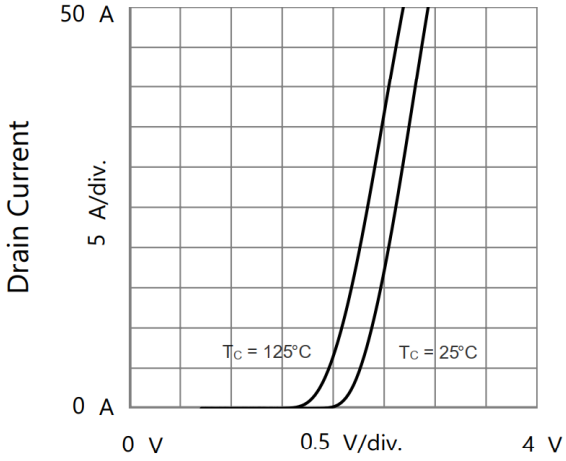
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

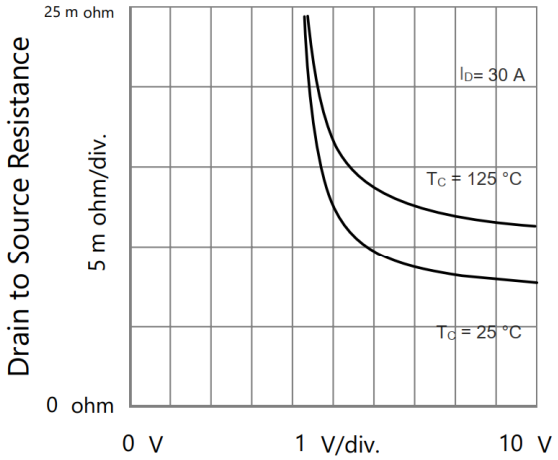
TYPICAL CHARACTERISTICS (25 C, unless otherwise noted)



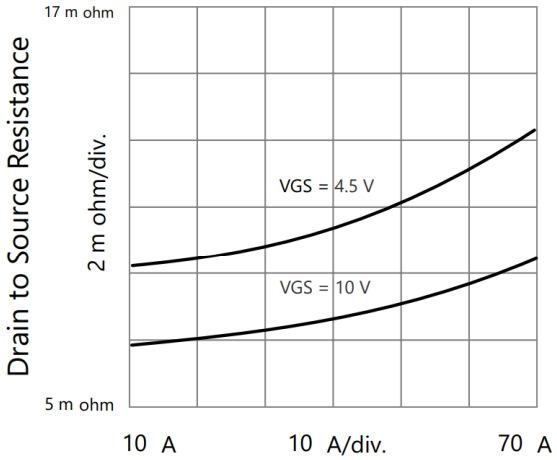
Drain to Source Voltage
Output Characteristics



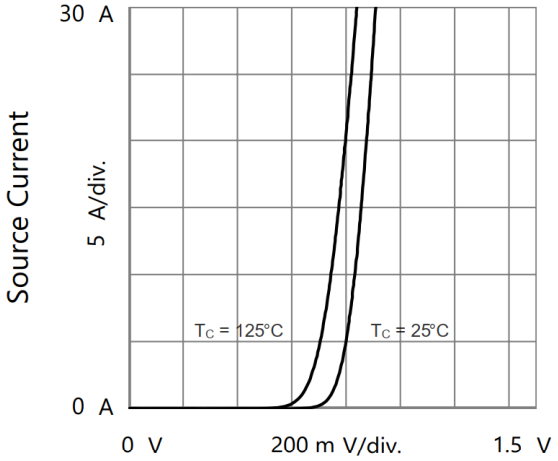
Gate to Source Voltage
Transfer Characteristics



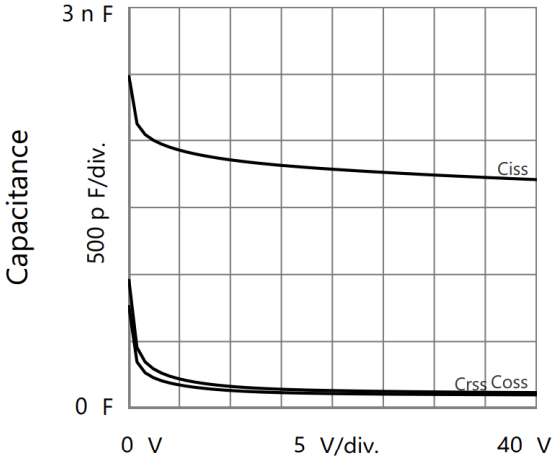
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



Drain Current
Drain to Source Resistance vs. Drain Current

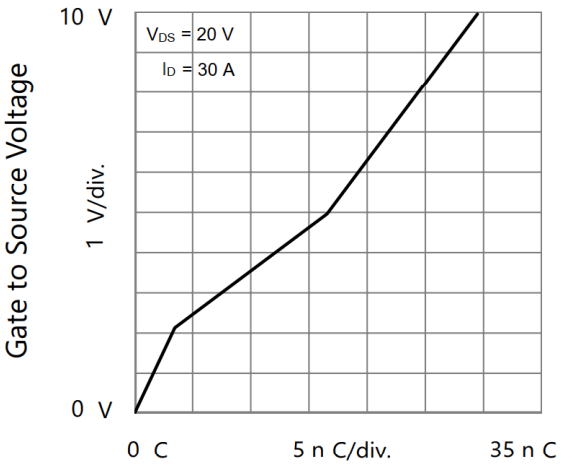


Source to Drain Voltage
Body Diode Forward Characteristics

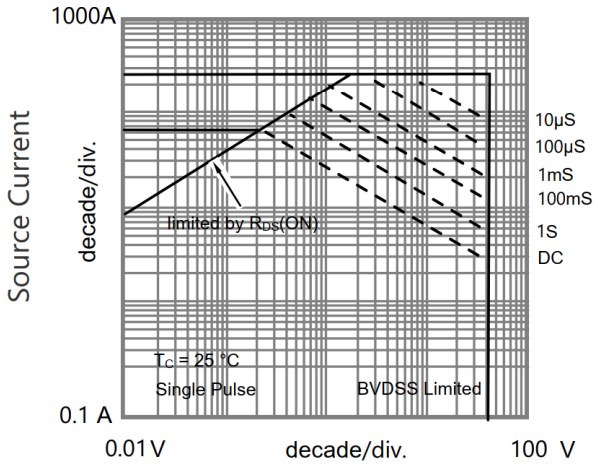


Drain to Source Voltage
Capacitances

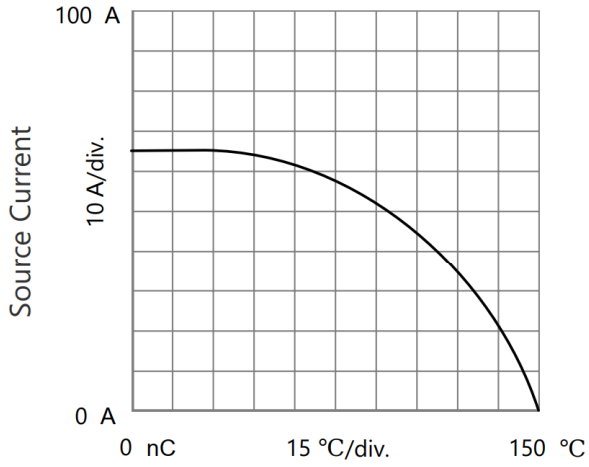
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



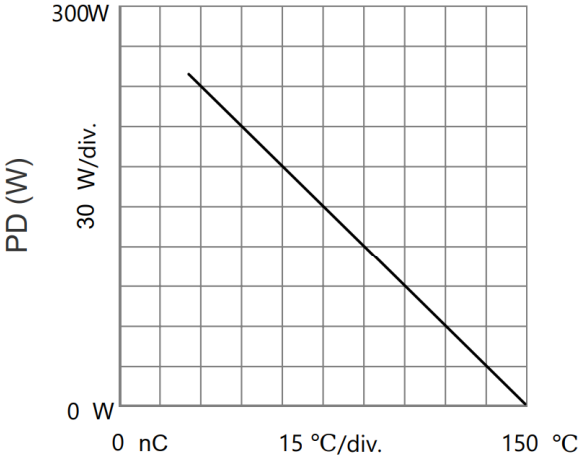
Gate to Source Voltage vs. GateCharge



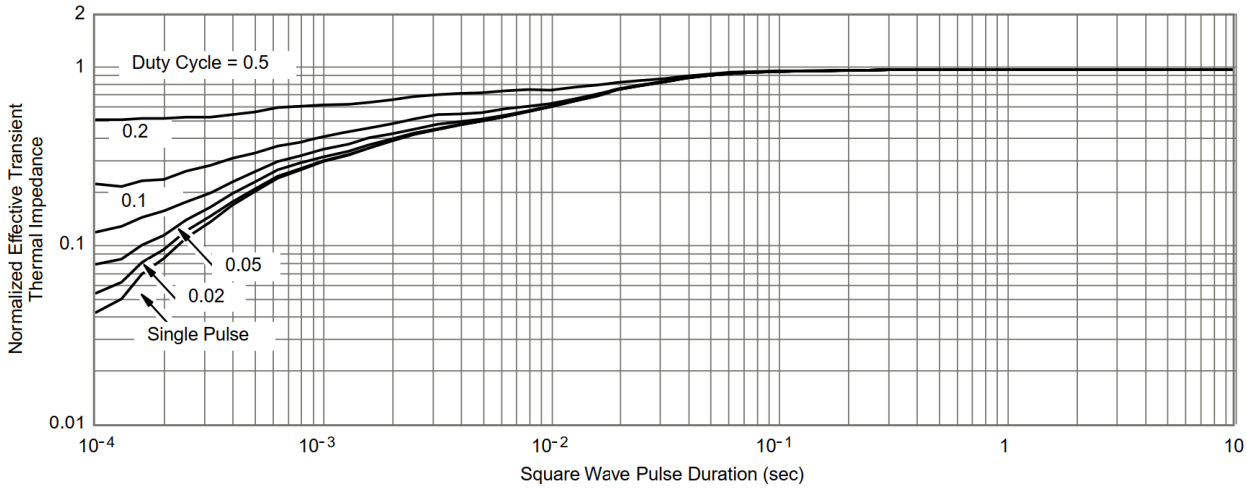
Safe Operating Area, Junction-to-Ambient



Current Derating

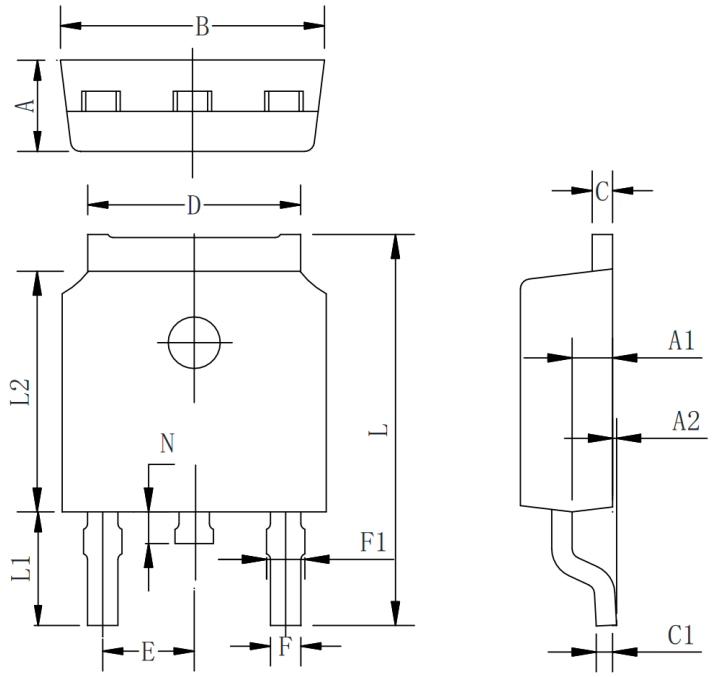


Power Derating



Normalized Thermal Transient Impedance, Junction-to-Case

TO-252-2L PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

Symbol	Min	Typ	Max
A	2.10	2.30	2.50
A1	0.88	1.01	1.16
A2	0.00	0.15	0.28
B	6.40	6.60	6.80
C	0.42	0.50	0.63
C1	0.42	0.50	0.63
D	5.08	5.32	5.65
E		2.286 TYP	
F	0.63	0.76	0.89
F1	0.64	0.86	1.08
L	9.30	9.90	10.80
L1	2.4	2.8	3.6
L2	5.90	6.10	6.55
N	0.57	0.80	1.05

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