

## N-Channel 50 V (D-S) MOSFET

### PRODUCT SUMMARY

V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)(Max.)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)
50	3 at V <sub>GS</sub> = 10 V	0.3	1.6 nC
	4 at V <sub>GS</sub> = 4.5 V		

### FEATURES

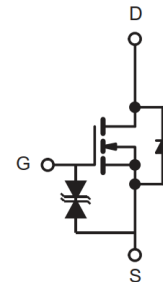
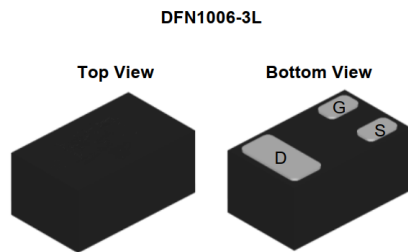
- DT-Trench Power MOSFET
- Surface Mount Package
- ESD Protection



**RoHS**  
COMPLIANT

### APPLICATIONS

- Battery operated systems
- Direct logic-level interface: TTL/CMOS



N-Channel MOSFET

### ABSOLUTE MAXIMUM RATINGS (T<sub>J</sub> = 25 °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V <sub>DS</sub>	50	V
Gate-Source Voltage	V <sub>GS</sub>	± 20	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	I <sub>D</sub>	T <sub>A</sub> = 25 °C	0.3
		T <sub>A</sub> = 70 °C	0.2
Pulsed Drain Current	I <sub>DM</sub>	1.52	A
Maximum Power Dissipation <sup>a</sup>	P <sub>D</sub>	T <sub>A</sub> = 25 °C	0.35
		T <sub>A</sub> = 70 °C	0.22
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

### THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) <sup>b</sup>	R <sub>thJA</sub>	357	°C/W

Notes:

a. T<sub>C</sub> = 25 °C.

b. Surface mounted on 1" x 1" FR4 board.

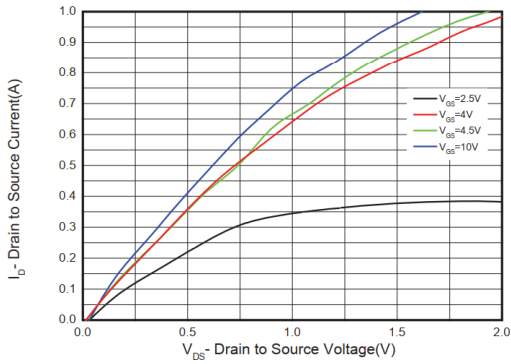
SPECIFICATIONS ( $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	50	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	0.8	-	2.5	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$	-	-	$\pm 10$	$\mu\text{A}$
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 240\text{ V}, V_{GS} = 0\text{ V}$	-	-	1	$\mu\text{A}$
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	0.3	-	-	A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 0.5\text{ A}$	-	-	3	$\Omega$
		$V_{GS} = 4.5\text{ V}, I_D = 0.2\text{ A}$	-	-	4	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 10\text{ V}, I_D = 0.2\text{ A}$	80	-	-	mS
<b>Dynamic <sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{ V}, V_{DS} = 25\text{ V}, f = 1\text{ MHz}$	-	25	-	$\mu\text{F}$
Output Capacitance	$C_{oss}$		-	12	-	
Reverse Transfer Capacitance	$C_{rss}$		-	5	-	
Total Gate Charge <sup>c</sup>	$Q_g$	$V_{DS} = 30\text{ V}, V_{GS} = 10\text{ V}, I_D = 0.1\text{ A}$	-	1.5	-	nC
Gate-Source Charge <sup>c</sup>	$Q_{gs}$		-	0.5	-	
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$		-	0.2	-	
<b>Switching Parameters</b>						
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 30\text{ V}, I_D = 0.2\text{ A},$ $V_{GS} = 10\text{ V}, R_g = 3.5\text{ }\Omega$	-	4.5	-	ns
Rise Time	$t_r$		-	2.4	-	
Turn-Off Delay Time	$t_{d(off)}$		-	16	-	
Fall Time	$t_f$		-	23	-	
<b>Drain-Source Body Diode Ratings and Characteristics <sup>b</sup> (<math>T_C = 25\text{ }^\circ\text{C}</math>)</b>						
Continuous Source Current	$I_S$	$T_C = 25\text{ }^\circ\text{C}$	-	-	0.3	A
Pulsed Source Current	$I_{SM}$		-	-	1.52	A
Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = 0.2\text{ A}, V_{GS} = 0\text{ V}$	-	-	1.2	V

**Notes**

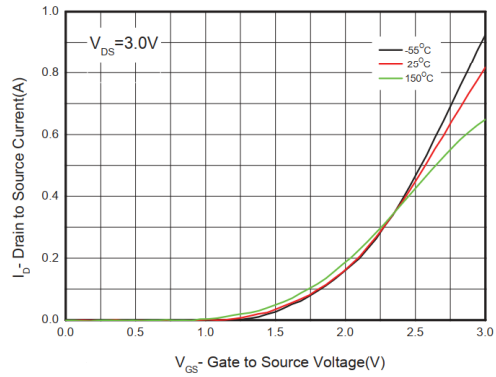
- Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

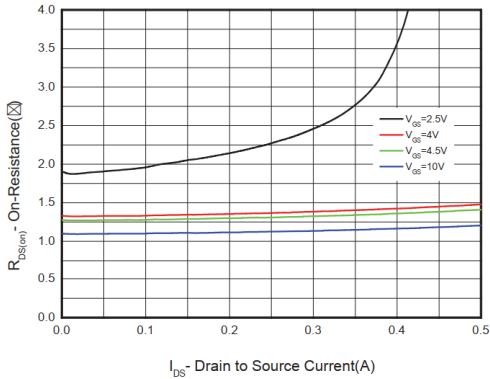
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



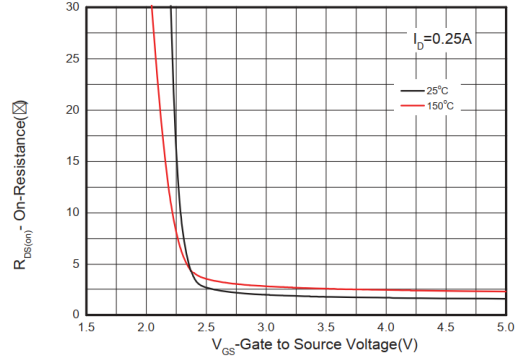
**Output characteristics**



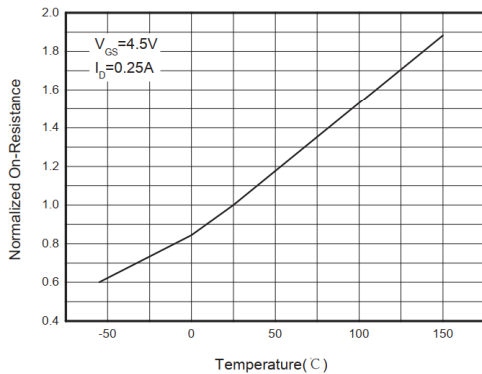
**Transfer characteristics**



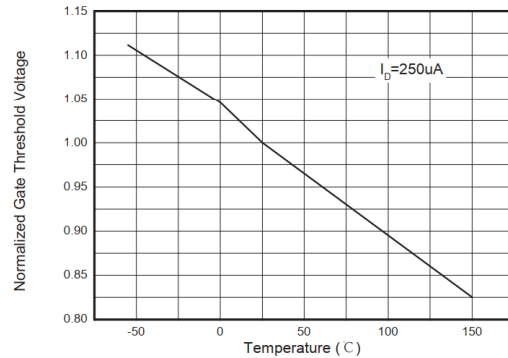
**On-Resistance vs. Drain current**



**On-Resistance vs. Gate-to-Source voltage**

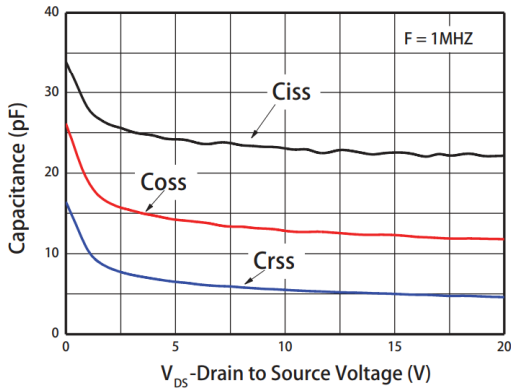


**On-Resistance vs. Junction temperature**

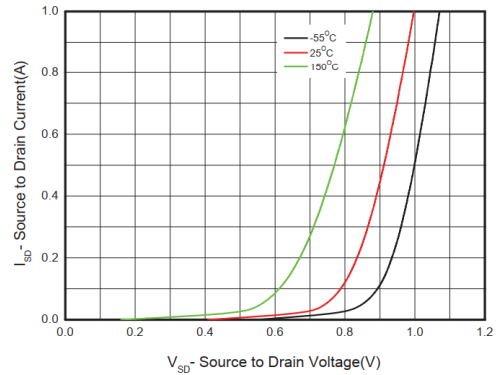


**Threshold voltage vs. Temperature**

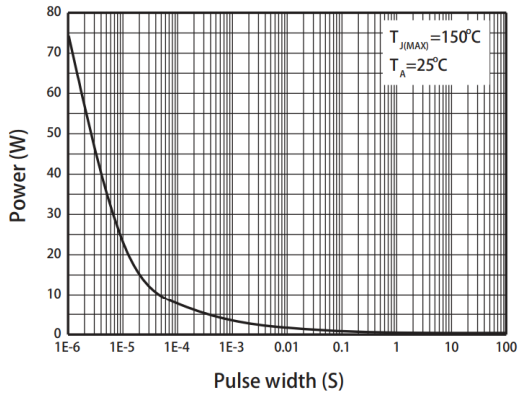
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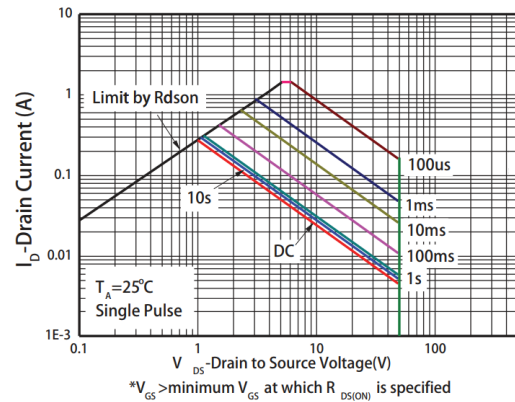
**Capacitance**



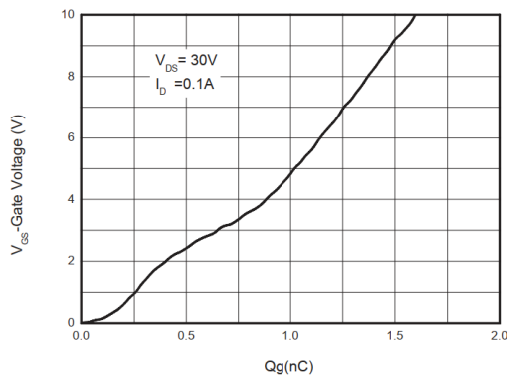
**Body diode forward voltage**



**Single pulse power**

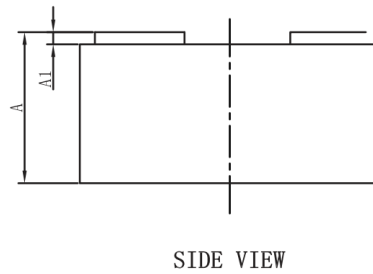
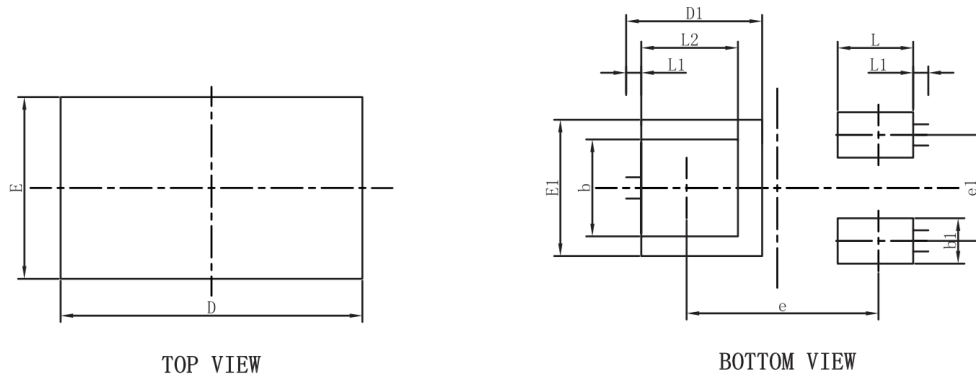


**Safe operating power**



**Gate charge Characteristics**

## DFN1006-3L PACKAGE OUTLINE



### COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX
A	0.400	0.500	0.600
A1	0.000	0.050	0.150
D	0.850	1.000	1.150
E	0.450	0.600	0.750
D1	0.450REF		
E1	0.450REF		
b	0.200	0.350	0.600
b1	0.050	0.150	0.250
e	0.635REF		
e1	0.200	0.300	0.500
L	0.150	0.250	0.350
L1	0.050REF		
L2	0.150	0.300	0.400

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