

P-Channel 15 V (D-S) MOSFET

PRODUCT SUMMARY

V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A) ^a	Q _g (Typ.)
-15	13.5 at V _{GS} = - 4.5 V	-11.5	16 nC
	19.6 at V _{GS} = - 2.5 V		

FEATURES

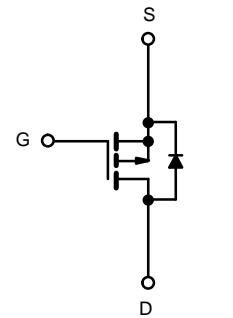
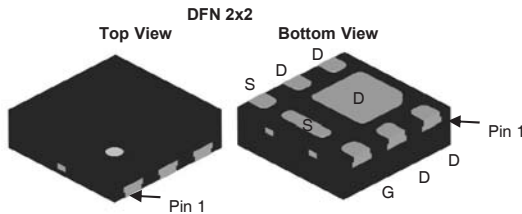
- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested
- Low Gate Charge
- High Power and current handling capability



RoHS
COMPLIANT

APPLICATIONS

- PWM Application
- Load Switch
- Power management



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V _{DS}	-15	V
Gate-Source Voltage	V _{GS}	± 8	
Continuous Drain Current (T _J = 150° C) ^a	I _D	T _A = 25 °C	-11.5
		T _A = 100 °C	-7.3
Pulsed Drain Current ^b	I _{DM}	-46	A
Maximum Power Dissipation ^c	P _D	T _A = 25 °C	3
		T _A = 100 °C	1.2
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to + 150	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) ^d	R _{thJA}	41.6	°C/W

Notes

- Calculated continuous current based on maximum allowable junction temperature.
- Repetitive rating; pulse width limited by max. junction temperature.
- P_d is based on max. junction temperature, using junction-case thermal resistance.
- The value of R_{thJA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C.

SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	-15	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-0.4	-	-1	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 8\text{ V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -12\text{ V}, V_{GS} = 0\text{ V}$	-	-	-1	μA
		$V_{DS} = -12\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^\circ\text{C}$	-	-	-100	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq -5\text{ V}, V_{GS} = -5\text{ V}$	-10	-	-	A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = -4.5\text{ V}, I_D = -5\text{ A}$	-	13.5	18	m Ω
		$V_{GS} = -2.5\text{ V}, I_D = -4\text{ A}$	-	19.6	26	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -5\text{ V}, I_D = -5\text{ A}$	-	14	-	S
Dynamic ^b						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}, V_{DS} = -10\text{ V}, f = 1\text{ MHz}$	-	1450	-	pF
Output Capacitance	C_{oss}		-	320	-	
Reverse Transfer Capacitance	C_{rss}		-	280	-	
Total Gate Charge ^c	Q_g	$V_{DS} = -10\text{ V}, V_{GS} = -4.5\text{ V}, I_D = -5\text{ A}$	-	16	-	nC
Gate-Source Charge ^c	Q_{gs}		-	3.5	-	
Gate-Drain Charge ^c	Q_{gd}		-	4.2	-	
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = -10\text{ V}, R_g = 3\text{ }\Omega, R_L = 2\text{ }\Omega$ $V_{GS} = -4.5\text{ V}$	-	16	-	ns
Rise Time ^c	t_r		-	65	-	
Turn-Off Delay Time ^c	$t_{d(off)}$		-	72	-	
Fall Time ^c	t_f		-	63	-	
Drain-Source Body Diode Ratings and Characteristics ^b ($T_J = 25\text{ }^\circ\text{C}$)						
Continuous Source-Drain Diode Current	I_S	$T_A = 25\text{ }^\circ\text{C}$	-	-	-11.5	A
Pulsed Current	I_{SM}		-	-	-46	A
Forward Voltage ^a	V_{SD}	$I_F = -5\text{ A}, V_{GS} = 0\text{ V}$	-	-	-1.2	V

Notes

- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

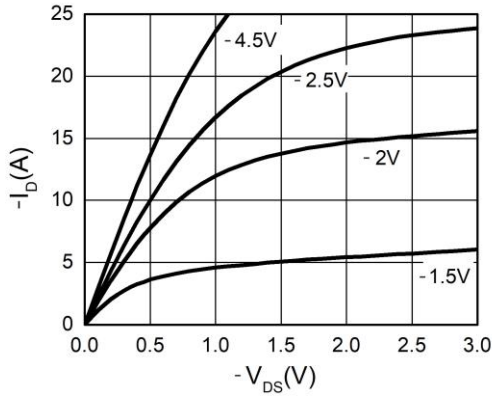


Figure 1. Output Characteristics

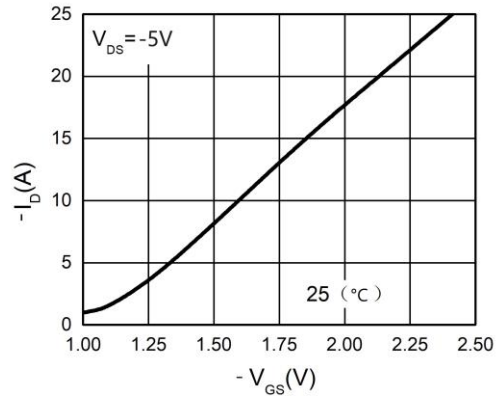


Figure 2. Transfer Characteristics

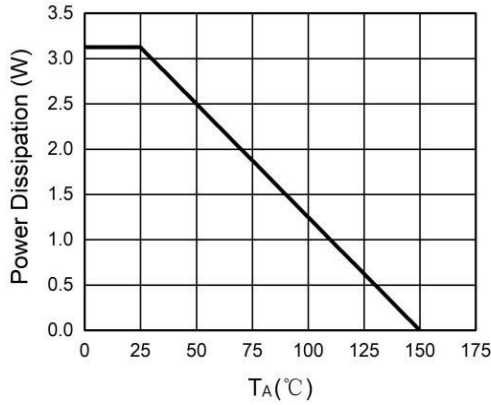


Figure 3. Power Dissipation

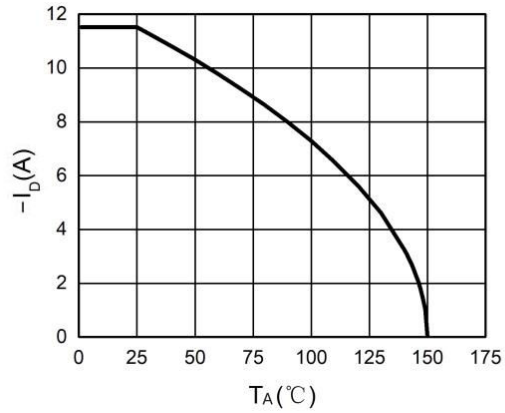


Figure 4. Drain Current

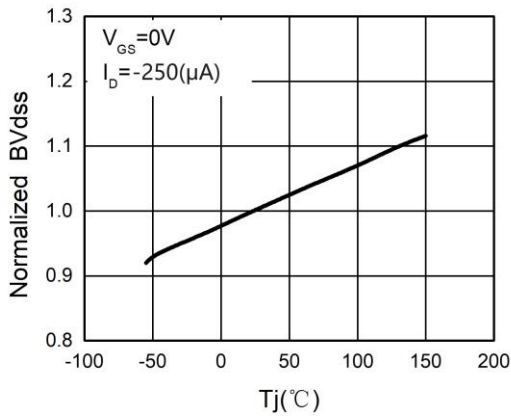


Figure 5. BV_{DS} vs Junction Temperature

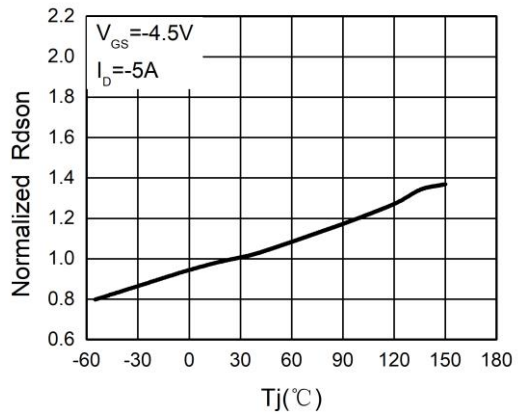


Figure 6. $R_{DS(ON)}$ vs Junction Temperature

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

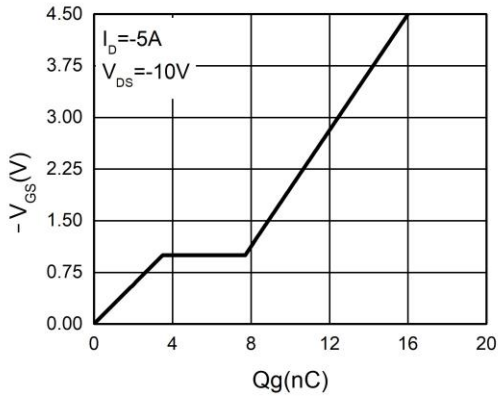


Figure 7. Gate Charge Waveforms

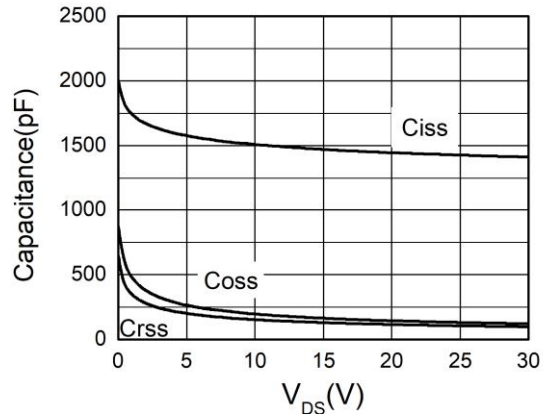


Figure 8. Capacitance

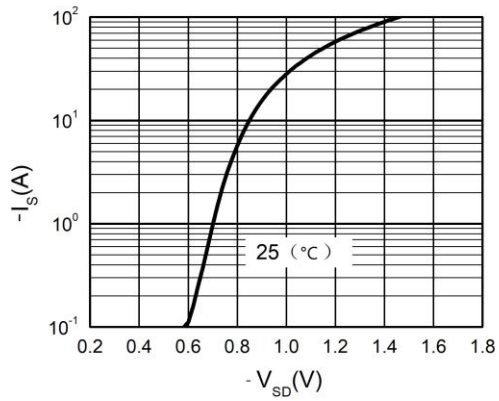


Figure 9. Body-Diode Characteristics

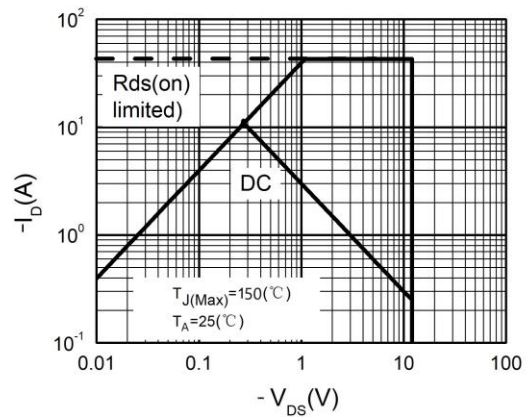
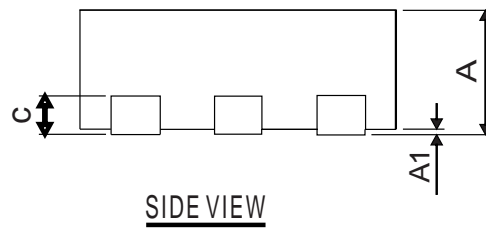
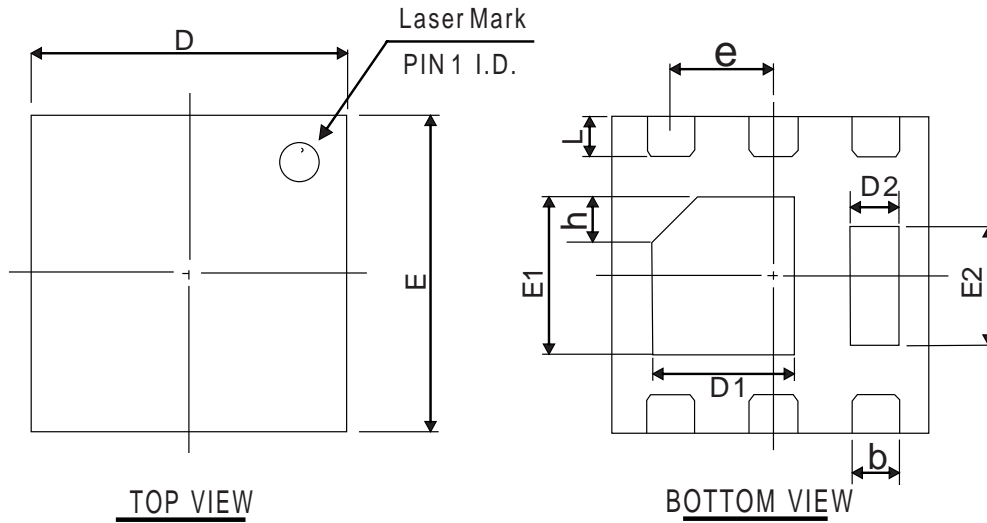


Figure 10. Maximum Safe Operating Area

DFN 2X2 PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX
A	0.60	0.60	0.90
A1	0.00	0.02	0.10
b	0.15	0.25	0.40
D	1.80	2.00	2.25
E	1.80	2.00	2.25
D1	0.70	0.90	1.10
E1	0.75	1.00	1.20
D2	0.15	0.30	0.45
E2	0.45	0.75	0.95
L	0.15	0.25	0.40
h	0.15	0.25	0.40
c	0.203 REF		
e	0.65 BSC		

Other thickness dimensions are as follows

A	0.50	0.55	0.60
A	0.40	0.45	0.50

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